



GP 2819
Dkt. No 016910-0476

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Nobuo FUDANUKI et al.

Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH
MIXED GATE ARRAY & STANDARD CELL

Appl. No.: 09/963,735

Filing Date: 09/27/2001

Examiner: D. Le

Art Unit: 2819

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant hereby petitions the Commissioner under 37 C.F.R. §1.136(a) for a one-month extension of time for response in the above-identified application for the period required to make the attached response timely.

The extension fee for response within the first month is \$110.00. A check for this amount is enclosed herewith.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date 3/19/03

FOLEY & LARDNER
Customer Number: 22428



22428

PATENT TRADEMARK OFFICE

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